

Scheme of Teaching and Examination for

3rd Semester of 3 Years Diploma in Electronics & Communication Engineering

Duration of Semester : **14 Weeks**

Student Contact Hours : **36 Hrs**

Total Marks : **800**

Effective from : 2017 -18 Session

Sl. No.	Name of Subject	Subject Code	Subject	Teaching Scheme			Examination Scheme					
				L	T	P	Hours of Exam	Full Marks of Subject	Final Exam / committee marks	Internal Assessment	Pass Marks Final / Ext. Exam	Pass Marks in Subjects
1.	Math III	301	Theory	4		-	3	100	80	20	26	40
2.	Electronic Devices and circuits	ECE303	Theory	3	-	-	3	100	80	20	26	40
3.	Electrical Technology	ECE304	Theory	3	-	-	3	100	80	20	26	40
4.	Elect & Electronic Measurement	ECE305	Theory	3	-	-	3	100	80	20	26	40
5.	Electromagnetic field Theory	ECE306	Theory	3	-	-	3	100	80	20	26	40
6.	Electronic Devices and circuits Lab	ECE307	Practical	-	-	4	4	100	80	20		40
7.	Electrical Technology Lab	ECE308	Practical	-	-	2	4	50	40	10		20
8.	Elect & Electronic Measurement Lab	ECE309	Practical			2	4	50	40	10		20
9.	Electromagnetic field Theory Lab	ECE310	Sessional			2		50	30	20	-	25
10.	Development of Life Skills II	302	Sessional	-	-	4		50	30	20	-	25
Total Hours of Teaching per week :				16		14						

Total Marks : Theory : Practical : Sessional :
 L : Lecture, T : Tutorial P : Practical

- Note:
1. Period of Class hours should be of 1 hrs duration as per AICTE norms.
 2. Remaining Hrs every week has been marked for students for Library and Student Centered Activities.
 3. Drawing / Graphics / Practical / Sessional examinations will be held at parent institution.
 4. Board will depute examiner for Practical examination.
 5. Regarding sessional examination the parent institution will form a three member committee and this committee will examine the sessional records and hold viva of the examinee for 60 % marks allotted to the subject. Marks for remaining 40 % will be provided by the Faculty concerned on the basis of evaluation of each job / work throughout the semester.

